



PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Bernardo A. Elayda III et al.
Assignee: Xilinx, Inc.
Title: "Wireless Programmable Logic Devices"
Serial No.: 09/823,591 File Date: 03-30-01
Examiner: Unknown Art Unit: 2681
Docket No.: X-851 US Conf. No.: 6851

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Substitute for Form 1449A.

Pursuant to rule 37 CFR 1.97(e)(1), Applicants certify that "each item of information contained in (this) Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of (this) information disclosure statement."

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

No fee is due with this communication.

The Commissioner is hereby authorized to charge any additional fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

Kim Kanzaki
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D. C. 20231, on February 20, 2003.

Pat Slaback
Name

Signature